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APPLICATION NO. FILING DATE 09/932,754 08/17/2001		FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO. 5384	
		Emil Kamieniecki	QCS-001DV3		
21323 7:	590 05/09/2002				
TESTA, HURWITZ & THIBEAULT, LLP HIGH STREET TOWER 125 HIGH STREET			EXAMINER		
			HOLLINGTON, JERMELE M		
BOSTON, MA	02110		ART UNIT	PAPER NUMBER	
			2829		

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application No.		Applicant(s)				
Office Action Summary		09/932,754		KAMIENIECKI ET	AL.			
		Examiner		Art Unit				
		Jermele M. Holli		2829				
The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply								
A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely. - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication. - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). - Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).								
Status								
1)🛛	1) Responsive to communication(s) filed on <u>17 August 2001</u> .							
2a)	This action is FINAL . 2b)⊠ This action is non-final.							
3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213. Disposition of Claims								
4)⊠ Claim(s) <u>53-57</u> is/are pending in the application.								
4a) Of the above claim(s) is/are withdrawn from consideration.								
5) Claim(s) is/are allowed.								
6)⊠ Claim(s) <u>53-57</u> is/are rejected.								
7) Claim(s) is/are objected to.								
8) Claim(s) are subject to restriction and/or election requirement. Application Papers								
9) The specification is objected to by the Examiner.								
10) ☐ The drawing(s) filed on is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.								
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).								
11) ☐ The proposed drawing correction filed on is: a) ☐ approved b) ☐ disapproved by the Examiner.								
If approved, corrected drawings are required in reply to this Office action.								
12)☐ The oath or declaration is objected to by the Examiner.								
Priority under 35 U.S.C. §§ 119 and 120								
13) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).								
a) ☐ All b) ☐ Some * c) ☐ None of:								
1	1. Certified copies of the priority documents have been received.							
2	2. Certified copies of the priority documents have been received in Application No							
 Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received. 								
14) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).								
a) ☐ The translation of the foreign language provisional application has been received. 15)☑ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.								
Attachment(s)								
2) Notice 3) Informa	of References Cited (PTO-892) of Draftsperson's Patent Drawing Review (PTO-948) ation Disclosure Statement(s) (PTO-1449) Paper No(s) 3.	4)		(PTO-413) Paper No atent Application (PT				
U.S. Patent and Trad PTO-326 (Rev.		tion Summary		Part o	f Paper No. 4			

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DETAILED ACTION

Claim Objections

1. Claim 53 is objected to because of the following informalities: in line 10, "said surface voltage sensor" should be --said surface photovoltage sensor--. Appropriate correction is required.

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 3. Claims 53-57 are rejected under 35 U.S.C. 102(b) as being anticipated by Kamieniecki et al (5091691).

Regarding claim 53, Kamieniecki discloses [see fig. 17] an apparatus for making surface photovoltage measurements of a semiconductor comprising a sealed chamber (represented as enclosure 197) [see column 12 lines 15-19] for processing the semiconductor wafer (represented as specimen 11) [see column 4 lines 34-37 and column 6 lines 32-36] and a head assembly (represented as reference electrode assembly 199) having a modulated light source (43) inherently exposing at least a portion of the semiconductor wafer (11) to light having a wavelength and modulated at a frequency [see column 7 lines 29-35 and lines 49-62 and column 12 lines 39-42] and a surface photovoltage sensor (represented as reference electrode 203) inherently detecting a surface photovoltage induced at the surface of the semiconductor wafer (11) in response to the light [via light source 43] without contacting the wafer (11) [see column

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12 lines 39-50], wherein the photovoltage is used to calculate an electrical property of the semiconductor wafer (11) [see Abstract], said surface photovoltage sensor (203) of head assembly (199) located within the sealed chamber (197).

Regarding claims 54-56, Kamieniecki inherently discloses the sealed chamber (197) as a reduced pressure chamber, a chemically reactive gas chamber or an inert environment chamber [see column 12 lines 15-16 and column 13 lines 42-46].

Regarding claim 57, Kamieniecki discloses [see fig. 17] the head assembly (199) is entirely located within the sealed chamber (197).

Conclusion

4. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Lagowski (5369495), Lowell et al (5963783) and Hause et al (6166354) disclose a method and apparatus for testing of a semiconductor wafer.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jermele M. Hollington whose telephone number is (703) 305-1653. The examiner can normally be reached on M-F (9:00-3:30 EST) First Friday Off.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Michael Sherry can be reached on (703) 308-1680. The fax phone numbers for the organization where this application or proceeding is assigned are (703) 308-7722 for regular communications and (703) 308-7382 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 308-1782.

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May 5, 2002

Jermele M. Hollington Examiner Art Unit 2829

Mihales 5/6/02 MICHAEL SHERRY

SUPERVISORY PATENT EXAMINER TECHNOLOGY CENTER 2800